



CALL FOR PAPERS
SPECIAL SESSION ON

“Signal and Power Integrity Automation Design and Modelling”
CODIT'18

April 10-13, 2018 –Thessaloniki, Greece

Session Co-Chairs :

Dr. Wael Dghais, University of Sousse, Tunisia

Dr. Muhammad Alam, Department of CSSE, Xi'an Jiaotong-Liverpool University, Suzhou, China

Prof. Yuanfang chen, School of Cyberspace, Hangzhou Dianzi University, China

Session description

This special session deals with the problem of real-time control, modelling, and processing of front-ends wired electrical and optical or wireless RF and microwave communication I/O links. The development of such system must follow an adaptive, reflexive, intelligent, and fast reconfigurable modelling design.

The goal of this session is to bring together developers and researchers from industry and academia in order to meet and discuss solutions, scientific results, and methods in solving intriguing problems in design and control automation along with reduced order and real time modelling algorithms for signal and power integrity, modelling algorithms.

The topics of interest include, but are not limited to:

- Advanced signaling/modulation techniques and transmitter's architectures
- High-speed and ultra-low-power communication I/O links design and modeling
- Reduced order modelling algorithms for signal and power integrity
- RF/microwave and mixed-signal power amplifiers
- I/O buffers behavioral modelling

SUBMISSION

Papers must be submitted electronically for peer review through PaperCept by **December 10, 2017**: <http://controls.papercept.net/conferences/scripts/start.pl>. In PaperCept, click on the **CoDIT 2018 link “Submit a Contribution to CoDIT'18” and follow the steps.**

All papers must be written in English and should describe original work. The length of the paper is limited to a maximum of 6 pages (in the standard IEEE conference double column format).

DEADLINES

December 10, 2017: deadline for paper submission

February 4, 2018: notification of acceptance/reject

February 28, 2018: deadline for final paper and registration.